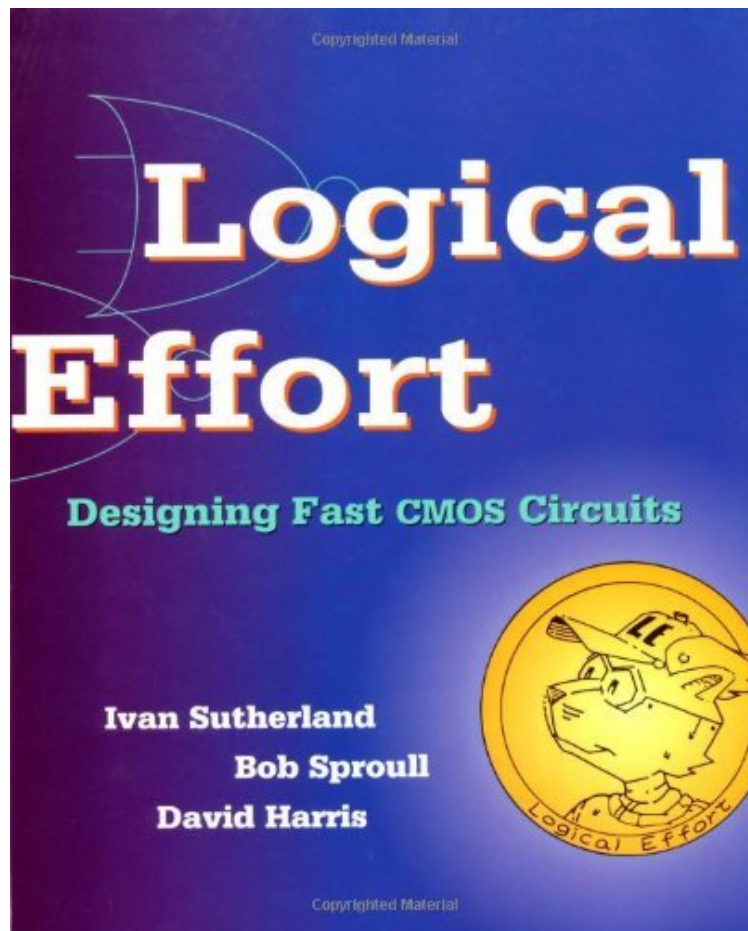



[Library ebook] Logical Effort: Designing Fast CMOS Circuits (The Morgan Kaufmann Series in Computer Architecture and Design)

## Logical Effort: Designing Fast CMOS Circuits (The Morgan Kaufmann Series in Computer Architecture and Design)

Von Ivan Sutherland, Robert F. Sproull, David Harris  
ePub | \*DOC | audiobook | ebooks | Download PDF



 Download

 Read Online

Produktinformation Veröffentlicht am: 1999-02-09 Erscheinungsdatum: 1999-02-09 File Name:  
B00AAPRHE2 | File size: 53.Mb

Von Ivan Sutherland, Robert F. Sproull, David Harris : Logical Effort: Designing Fast CMOS Circuits (The Morgan Kaufmann Series in Computer Architecture and Design) before purchasing it in order to gage whether or not it would be worth my time, and all praised Logical Effort: Designing Fast CMOS Circuits (The Morgan Kaufmann Series in Computer Architecture and Design):