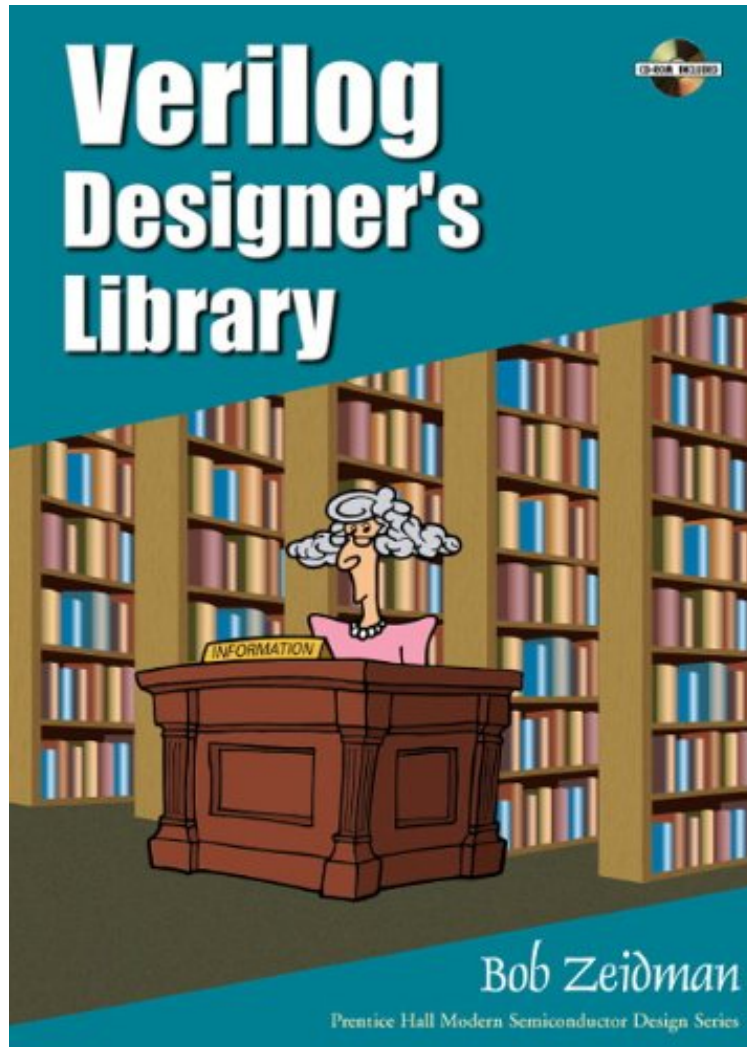


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Verilog Designer's Library (Prentice Hall Modern Semiconductor Design Series)

Von Bob Zeidman

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Von Bob Zeidman : Verilog Designer's Library (Prentice Hall Modern Semiconductor Design Series) before purchasing it in order to gage whether or not it would be worth my time, and all praised Verilog Designer's Library (Prentice Hall Modern Semiconductor Design Series):

KundenrezensionenHilfreichste Kundenrezensionen2 von 2 Kunden fanden die folgende Rezension hilfreich. A good library of modelsVon L. W. NoronhaI was looking for models for a dual-port RAM and a FIFO. I couldn't find any book that had examples of either one of them. This book does and in great detail. That was worth the price of the book

for me.

Kurzbeschreibung Ready-to-use building blocks for integrated circuit design. Why start coding from scratch when you can work from this library of pre-tested routines, created by an HDL expert? There are plenty of introductory texts to describe the basics of Verilog, but Verilog Designer's Library is the only book that offers real, reusable routines that you can put to work right away. Verilog Designer's Library organizes Verilog routines according to functionality, making it easy to locate the material you need. Each function is described by a behavioral model to use for simulation, followed by the RTL code you'll use to synthesize the gate-level implementation. Extensive test code is included for each function, to assist you with your own verification efforts. Coverage includes: Essential Verilog coding techniques Basic building blocks of successful routines State machines and memories Practical debugging guidelines Although Verilog Designer's Library assumes a basic familiarity with Verilog structure and syntax, it does not require a background in programming. Beginners can work through the book in sequence to develop their skills, while experienced Verilog users can go directly to the routines they need. Hardware designers, systems analysts, VARs, OEMs, software developers, and system integrators will find it an ideal sourcebook on all aspects of Verilog development.